

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S7	236	('LDMOS' NEAR3 TRANSISTOR) WITH SOURCE	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/28 09:28
S8	150	S7 AND (IMPLANTATION IMPLANTING IMPLANTED IMPLANT)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/28 09:31
S9	109	S7 AND (IMPLANTATION IMPLANTING IMPLANTED IMPLANT) WITH (MASK\$4)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/28 08:49
S10	109	S9 AND (IMPLANTATION IMPLANTING IMPLANTED IMPLANT MASK\$4)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/28 08:49
S11	66	S10 AND (GATE NEAR5 MASK\$4)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/28 08:50
S12	826	('LDMOS' LATERAL ADJ 'DMOS') NEAR3 TRANSISTOR	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/28 10:45
S13	98	S12 and ((p adj body p adj region) with source)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/28 10:46
S14	93	S13 AND (IMPLANTATION IMPLANTING IMPLANTED IMPLANT)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/28 09:31
S15	61	S13 AND (IMPLANTATION IMPLANTING IMPLANTED IMPLANT) with mask\$3	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/28 10:46
S16	55	S15 AND (gate with mask\$3)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/28 09:32

S17	1325	('LDMOS' LATERAL ADJ 'DMOS')	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/28 10:45
S18	153	S17 and ((p adj body p adj region) with source)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/28 10:46
S19	91	S18 AND (IMPLANTATION IMPLANTING IMPLANTED IMPLANT) with mask\$3	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/06/28 10:46